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Modul 05: Transistor

bil dari buku "Fundamentals of Analog Circuits, 2nd edition" oleh Thomas L Floyd dan David Buchla) 4 Emitter Bias merupakan rangkaian yang sangat stabil, namun membutuhkan catu daya positif dan negatif Rangkaian ini ditunjukkan oleh Gambar 11 Gambar 11: Rangkaian Emitter bias (diambil dari buku "Fundamentals of Analog Circuits, 2nd edi-

Modul 04: Op-Amp

amp (diambil dari buku "Fundamentals of Analog Circuits, 2nd edition" oleh Thomas L Floyd dan David Buchla) inverting, arus yang berasal sinyal masukan yang melewati hambatan R_i adalah $I_i = V_i / R_i = V_i / R_i$ (4) Akibat dari kondisi virtual ground dan impedansi masukan yang tinggi, maka tidak ada arus yang akan masuk ke op-amp dan semua

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